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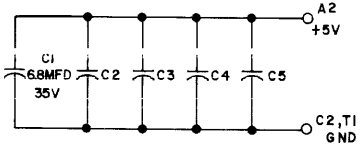

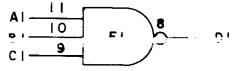

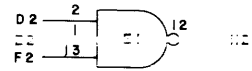

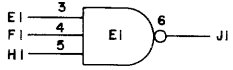

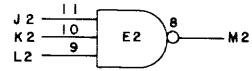

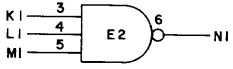

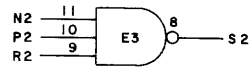

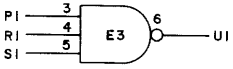

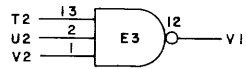

UNLESS OTHERWISE INDICATED:
IC'S ARE DEC74H10
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V
CAPACITORS ARE .01 MFD 50V

REVISIONS			
CHK	CHG NO	REV

DRN		DATE 3-3-68
CHK BY		DATE 3-3-68
ENG	NEO	DATE 9-7-69
PROD.		DATE

TRANSISTOR & DIODE CONVERSION CHART

DEC	EIA	DEC	EIA

digital
EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE 3 INPUT NAND GATE
M135

SIZE	CODE	NUMBER	REV
B	CS	M135-0-1	

PRINTED CIRCUIT REV. A